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In 1449A/PTO Substitute for form 1449A/PTO Application Number 10/757,925 INFORMATION DISCLOSURE Filing Date January 16, 2004 STATEMENT BY APPLICANT First Named Inventor Craig C. HANSEN, et al. 2183 Group Art Unit (use as many sheets as necessary) **Examiner Name** TSAL HENRY of 2 Attorney Docket Number 43876-158 Sheet 1

			U.S. PATEN	T DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-5,325,495 A	06-28-1994	McLellan	

		FORE	IGN PATENT DOC	UMENTS		
Examiner Initials*	Cite No.	Foreign Patent Document Country Code3 Number 4- Kind Code5	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		(if known) EP 0 649 085 A1	04-19-1995	Bluhm / Cyrix Corp.	riguies Appea	
		EP 0 653 703 A1	05-17-1995	Yung / Sun Microsystems, Inc.		
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Substitute for form 1449A/PTO				Complete if Known		
TATIC	ODM A TYON!	DICC	T OSIME	Application Number	10/757.925	
INFORMATION DISCLOSURE				Filing Date	January 16, 2004	
STATEMENT BY APPLICANT (use as many sheets as necessary)			PLICANT	First Named Inventor	Craig C. HANSEN, et al.	
				Group Art Unit	2183	
				Examiner Name	TSAI. HENRY	
Sheet	2	of	2	Attorney Docket Number	43876-158	

		OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	No. item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number publisher, city and/or country where published.					
		Fuller et al., "The PowerPC 604 <sup>TM</sup> Microprocessor – Multimedia Ready," Circuits and Systems, 1995, Proceedings of the 38 <sup>th</sup> Midwest Symposium on Rio de Janeiro, Brazil (Aug. 13-16, 1995), New York, NY, IEEE, vol. 2, 1135-38 (Aug. 13, 1995) [XP010165331]				
		Gwennap, "MIPS R10000 Uses Decoupled Architecture – High-Performance Core Will Drive MIPS High-End for Years," Microprocessor Report, vol. 8, no. 14 (Oct. 24, 1994), MicroDesign Resources (1994) [XP002219607]				
		Murakami et al., "SIMP (Single Instruction Stream / Multiple Instruction Pipelining): A Novel High-Speed Single-Processor Architecture," Computer Architecture News, ACM, New York, NY, vol. 17, no. 3, 78-85 (Jun. 1, 1989) [XP000035291]				
		"UltraSPARC Multimedia Capabilities On-Chip Support for Real-Time Video and Advanced Graphics," Sun Microsystems, Inc., 1-8 (1994) [XP002177546]				
_		P27838EP-D3-PCT Extended European Search Report dated July 3, 2008 in Application No. / Patent No. 07111350.0- 1243 / 1873654				
		P27838EP-D6-PCT Extended European Search Report dated June 27, 2008 in Application No. / Patent No. 07111348.4-1243 / 1873629				
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Examiner		Date				

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